Nor-Tech Announces Free Trial of AMD EPYC

Nor-Tech integrated AMD EPYC into its no cost to use demo cluster. The utility joins of full suite of HPC applications/platforms available on the demo cluster.

MINNEAPOLIS, MINN., UNITED STATES, September 19, 2017 /EINPresswire.com/ -- MINNEAPOLIS---Nor-Tech just integrated AMD EPYC into its leading-edge demo cluster—enabling a free, no strings trial. The utility joins of full suite of applications and platforms in high demand by today's HPC cluster users that are available for trial on the demo cluster.

Nor-Tech President and CEO David Bollig said, "Our demo cluster is a key utility for clients and prospects alike. Because of this, we carefully vet everything that we integrate. We liked several aspects of AMD EPYC from the start and now that we have been able to witness it in action, we can see that this has the potential to shift the industry."

The AMD EPYC SoC (system on a chip) has the memory capacity and bandwidth to meet the processor core's high demand for data; and I/O bandwidth that matches the capacity of the CPU cores to move data to and from the network, spinning disks, NVMe storage, and graphics acceleration devices.

The AMD EPYC SoC bridges current performance gaps with innovations that efficiently support the needs of existing and future datacenters. Highlights include:

• Performance. The highest core count in an x86-architecture server processor, largest memory capacity, most memory bandwidth, and greatest I/O density are brought together with the right ratios to reach new levels of performance.

• Flexibility. Core count can be matched with application needs without compromising processor features.

• Security. AMD created the first dedicated security processor embedded in an x86-architecture server SoC. The processor manages secure boot, memory encryption, and secure virtualization on the SoC itself. Encryption keys never have to leave the processor.

• Features (per CPU) include:

o Up to 32 high-performance cores (64 threads): Boosts performance and compute density.
o Up to 2TB of DDR4 memory capacity (across 8 channels): Accelerates memory-intensive application performance.
o 128 Lanes of PCIe Gen 3: Extends server capabilities without incremental PCI switching.
o Integrated security subsystem: Protects and enables secure multi-tenancy per CPU/SoC.

The AMD EPYC SoC brings performance optimization to a higher level, delivering the highest core count and memory capacity; and the greatest memory bandwidth, and I/O density in the industry. According to AMD, the processor:
1. Supports up to 21.3 GBs per channel with DDR4-2666 x 8 channels (total 170.7 GBs), versus the Xeon E5-2699A v4 processor at 19.2 GBs with max DDR4-2400 x 4 channels (total 76.8 GBs). NAP-03
2. Offers up to 128 PCI Express high speed I/O lanes per socket, versus the Xeon E5-2699A v4 processor at 40 lanes per socket. NAP-05
3. Includes up to 32 CPU cores versus the Xeon E5-2699A v4 processor with 22 CPU cores. NAP-02

Nor-Tech's No cost trial of AMD EPYC is easily accessed via contact form or Live Chat.
Nor-Tech is on CRN's list of the top 40 Data Center Infrastructure Providers—joining ranks with IBM, Dell, Hewlett Packard Enterprise, and Lenovo. The company is renowned throughout the scientific, academic, and business communities for easy to deploy turnkey clusters and expert, no wait time support. All of Nor-Tech's technology is made by Nor-Tech in Minnesota and supported by Nor-Tech around the world. In addition to HPC clusters, Nor-Tech's custom technology includes workstations, desktops, and servers for a range of applications including CAE, CFD, and FEA. Nor-Tech engineers average 20+ years of experience and are responsible for significant high performance computing innovations. The company has been in business since 1998 and is headquartered in Burnsville, Minn. just outside of Minneapolis. To contact Nor-Tech call 952-808-1000/toll free: 877-808-1010. For more information about the demo cluster visit http://www.nor-tech.com/solutions/hpc/demo-cluster/.

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