

Truechip Successfully hosted TrueConnect 2017- The First Annual Technical Conference

A non -EDA based conference principally designed to address the opportunities and challenges in functional verification domain.

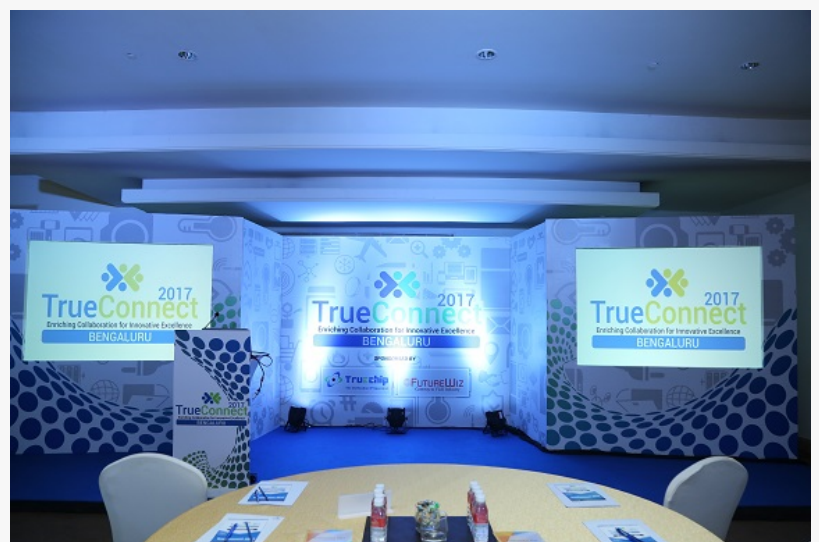
NEW DELHI, NEW DELHI, INDIA, December 13, 2017 /EINPresswire.com/ -- A non -EDA based conference principally designed to address the opportunities and challenges in functional [verification](#) domain.

[Truechip](#) - The Verification IP Specialist successfully hosted its 1st Annual Technical Conference, [TrueConnect 2017- Enriching Collaboration For Innovative Excellence](#), at Hotel Park Plaza, Bengaluru on December 7, 2017.

The event received an overwhelming response from semiconductor industry veterans and professionals and created a memorable experience for all. The conference witnessed more than 70 delegates from organizations like Western Digital, Cypress Semiconductors, Microsemi, NXP, Qualcomm, Infineon Technologies, Intel, Broadcom, Texas Instruments, MediaTek and many others.

On this occasion, Mr. Nitin Kishore, Founder & CEO, Truechip said “We wanted to host an open technical conference for design and verification professionals since this plays a vital role in chip design. TrueConnect is unlike other EDA conferences, since it does not focus on EDA tools and allows anybody from this domain to participate and submit papers”.

Adding to this Mr. Saurabh Agarwal, Head, Marketing and Sales, Truechip said “TrueConnect 2017, Bangalore edition was excellent. For TrueConnect 2018 edition we will have multiple tracks, keynotes and panel discussions”.



TrueConnect 2017 Conference



Mr. Sanjay Gupta- Vice President & Country Manager, NXP India, receiving token of appreciation from Mr. Nitin Kishore, CEO, Truechip

Highlights:

The Event opened with the welcome note from Mr. Nitin Kishore, followed by Keynote from Mr. Sanjay Gupta Vice President and Country Manager, NXP Semiconductor, and Uday Mishra, Director, ASIC Design Engineering Management, Western Digital. Both the speakers highlighted the importance of Verification in the semiconductor product development for safety critical applications such as robotics and autonomous and self-driving vehicles.

The Post lunch session witnessed a high voltage panel discussion on the topic- Design and verification sign-off and closure - quality and quantum. The Panelist included Mr. Amardeep Punhani- Director Automotive Digital IP for India operations, Mr. Sudhakar Reddy Amireddy, Verification Manager, Infineon Technologies, Mr. Omprakash Jha, Principal Engineer, Western Digital and Mr. Vishal Dalal, Verification Lead, Texas Instruments.

TrueConnect 2017 also had many technical paper presentations, adjudged by Mr. Prasad Joshi, Sr. Manager, IBM India and Mrs. Vandana Goel, Program Manager, MediaTek.

The best paper presentation 1st prize was given to Mr. Surya Nallathambi and Mr. Jayakanthan Arthanari from Western Digital for the paper titled "Implications & modus operandi applied for 100% FSM Coverage Closure". The 2nd prize was awarded to Mr. Giridhar Chelluri and Mr. Sukhjeet Singh from Truechip for the paper titled "Dynamic configurability in static blocks". The 3rd prize was given to Ms. Prajakta Rohom and Mr. Mukul Goyal from Open Silicon for the paper titled "Robust Approach to verify Training requirements of high bandwidth memory IPs"

The event also witnessed a show from Bangalore's no. 1 stand up comedian Dr. Jagdish Chaturvedi, lucky dips and gifts which provided the audience to enjoy after a full day conference.

About Truechip:

Truechip, The Verification IP Specialist, is a leading provider of Verification IP solutions, which aid to accelerate IP/ SOC design thus lowering the cost and the risks associated with the development of ASIC, FPGA and SOC. A privately held company with a solid and seasoned leadership, having global footprints and sales coverage across the globe. Truechip solutions serves over 50 customers in 7 countries including US, Israel and China in their Verification IP needs. With its global development center in India, the company has a strong presence in America, parts of Europe, Germany, China, Israel and Taiwan. The company participates in scores of global events including DAC, DVCon, MIPI DEVCON and SemIsrael. The team consists of over hundred engineers with an average experience of five years. Truechip has been serving customers for almost a decade in VLSI and offers Industry's first 24x5 technical support.

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Mr. Uday Mishra- Director Engineering (India), Western Digital ,receiving token of appreciation from Mr. Nitin Kishore, CEO, Truechip

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