

# Numem Introduces its SmartMem Endurance & Performance Engine for AI and IoT Applications

*The Time is now for efficient SOC  
Compute in Memory*

SUNNYVALE, CA, UNITED STATES,  
September 13, 2023 /

EINPresswire.com/ -- Numem Inc., a  
leading provider of NuRAM MRAM  
Memory and SmartMem SOC IP cores

and Chiplets, which are revolutionizing power consumption in AI systems from edge nodes to servers, is pleased to announce the release of its Patented SmartMem Endurance and Performance Engine IP Core.



The Patented SmartMem Endurance and Performance Engine IP Core dramatically improves memory endurance and performance as well as operating power for next-generation persistent memory (MRAM, ReRAM, PCRAM, etc...) and embedded as well as external Flash.

Numem's SmartMem Endurance and Performance Engine delivers intelligent memory management, boosting average endurance by 50x, average read times by 2.5x to 15x, and average write times by 7x to >100x, thanks to its SmartMem efficient data flow management.

As an example, when integrated with Numem NuRAM, it enables MRAM average Read Speeds of 2.5ns. And, for even lower power consumption, SmartMem has standby or deep sleep power modes.

The Endurance and Performance Engine is part of the SmartMem product line of SOC Subsystems. It is comprised of RTL Datapath Processing, RISC V Processor and associated SRAM, Standard Bus Interfaces, and a memory-dependent customizable interface.

"We are delighted to introduce the SmartMem Endurance and Performance Engine, which enables to significantly improve Endurance and Performance and reduce memory active power for any persistent memories and on any process technology," Jack Guedj , Numem President and CEO intimated.

## About Numem:

Numem, headquartered in Sunnyvale, California, is the leading provider of advanced SOC computing in Memory based on its patented NuRAM (MRAM), as well as SOC Subsystem IP Cores and Chiplets based on its patented, SmartMem technologies. Numem NuRAM provides an excellent SRAM and eFlash alternative for SoC Designers/ Architects with 2.5x smaller area and >20x lower leakage power than SRAM.

Numem SmartMem IP Cores and Chiplets provide an end-to-end solution that is disrupting the semiconductor industry by enabling ultra-low power, high-performance integration of persistent memory for a variety of memory, including MRAM, Numem NuRAM, RRAM/ReRAM, PCRAM, and embedded and standalone Flash.

Numem's IP Cores and Chiplets are used in IoT/Wearables, AI/Generative AI, Digital Security/Cameras, Autonomous Vehicles and Data Center applications. They enable Customers with a substantial reduction in system power and time to market.

Visit our website <http://www.numem.com> or contact us at [sales@numem.com](mailto:sales@numem.com)

## Contact :

Jack Guedj  
Chief Executive Officer  
Numem Inc.  
[jguedj@numem.com](mailto:jguedj@numem.com)

Koji Motomori  
Senior Director, Business Development, Numem Inc.  
[kmotomori@numeminc.com](mailto:kmotomori@numeminc.com)

---

This press release can be viewed online at: <https://www.einpresswire.com/article/655137771>

EIN Presswire's priority is source transparency. We do not allow opaque clients, and our editors try to be careful about weeding out false and misleading content. As a user, if you see something we have missed, please do bring it to our attention. Your help is welcome. EIN Presswire, Everyone's Internet News Presswire™, tries to define some of the boundaries that are reasonable in today's world. Please see our Editorial Guidelines for more information.

© 1995-2023 Newsmatics Inc. All Right Reserved.