

Monolithically integrated asynchronous optical recurrent accelerator

GA, UNITED STATES, May 7, 2025 /EINPresswire.com/ -- Scientists from China have developed a breakthrough asynchronous optical computing accelerator based on wavelength encoding. This architecture overcomes synchronization challenges in conventional optical recurrent processors, significantly reducing both the energy consumption of electronic components and the complexity of optical layout design. The chip enables efficient computing for large-scale AI tasks, such as DNA analysis and speech recognition, while achieving high energy efficiency.

(a) $\tau_i \geq \max_{q=1\dots i} (t_q + \sum_{k=q}^i \max_j \Delta t_{jk})$ $@\tau_3$ Wavelengt WRU Unfold by λ Δt_{12} Δt_{22} occ occ occ occ Asyn Qλ. @λ, chronous pro DACs @t1 DACs, drive (d) elength relay unit (WRU) (c) (e) Nonlinea

Modern artificial intelligence (AI) faces critical challenges in energy

Working principle of the asynchronous optical recurrent architecture.

consumption and processing latency due to the inherent limitations of electronic processors. Optical computing has emerged as a promising alternative; however, state-of-the-art systems depend heavily on frequent optical-electrical conversions and precise synchronization. This issue is particularly pronounced in recurrent AI models, where even slight temporal misalignments can accumulate over iterations, leading to severe signal degradation. These constraints not only impede large-scale parallel processing but also significantly limit overall computational efficiency.

In a recent publication in eLight, several scientists from Huazhong University of Science and Technology, China developed a monolithically integrated asynchronous optical recurrent accelerator. This novel system maps time sequences to wavelength channels, utilizing on-chip <u>wavelength relay units</u> (WRU) to eliminate the requirement for strict synchronization, thereby significantly improving computational efficiency.

"Our design circumvents the conventional reliance on high-speed electronic components for

synchronization," explains by these scientists. "By using wavelength relay units instead of traditional ADCs and DACs, we effectively reduce energy consumption while enabling efficient parallel signal processing."

The research team successfully implemented two pioneering optical computing models onchip:

 Optical Hidden Markov Model (OHMM) chip – Capable of analyzing DNA sequences with an impressive 99% accuracy, demonstrating potential for bioinformatics applications.
Optical Recurrent Neural Network (ORNN) chip – Achieved 87.7% accuracy in an 8-class speech recognition task, showcasing its feasibility in real-time AI-driven communication and language processing.

The monolithic integration of these models marks a breakthrough in optical computing, as both operate without the requirements of high-speed electrical synchronization, a long-standing challenge in photonic AI hardware.

The compact chip within 10 mm² footprint integrates hundreds of optical computing components, leveraging the inherent advantages of light-based processing. Unlike traditional electronic AI accelerators, which suffer from power-hungry transistors and heat dissipation issues, this optical accelerator functions with minimal energy overhead.

Furthermore, by utilizing wavelength multiplexing, the architecture efficiently handles highdimensional parallel computations. This capability is crucial for future AI applications, where processing speed and power efficiency are paramount.

"This breakthrough paves the way for efficient AI computing in autonomous vehicles, smart robotics, and beyond," note the scientists. "The ability to process massive data streams without electrical synchronization opens new avenues for real-time decision-making in AI systems."

Looking ahead, the team aims to scale the architecture for commercial AI hardware applications. The combination of high-speed and energy-efficient computation makes this optical accelerator a strong contender for next-generation AI hardware solutions.

With successive advancements in photonic integration and wavelength multiplexing, monolithic asynchronous optical accelerators may soon redefine the landscape of AI processing, bridging the gap between high-performance computing and sustainable energy consumption.

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