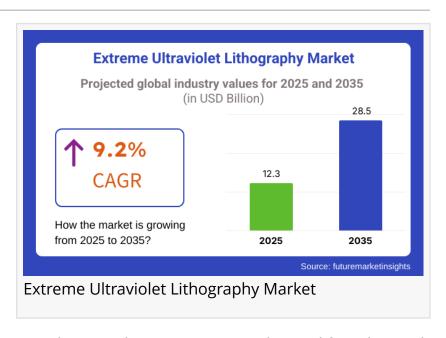


Extreme Ultraviolet Lithography Market to Reach USD 28.5 Billion by 2035, Growing at 9.2% CAGR

EUV lithography market is set for robust growth, fueled by chip innovation and demand for advanced semiconductor manufacturing.

NEWARK, DE, UNITED STATES, June 3, 2025 /EINPresswire.com/ -- The extreme ultraviolet lithography market is poised for robust growth in the coming decade, with the global valuation expected to rise from USD 12.3 billion in 2025 to USD 28.5 billion by 2035, reflecting a solid compound annual growth rate (CAGR) of 9.2%.



This growth is primarily fueled by the semiconductor industry's increasing demand for advanced lithography techniques capable of producing high-density, energy-efficient, and high-performance integrated circuits. As global demand for cutting-edge chips used in smartphones, AI, automotive electronics, and data centers continues to rise, EUV technology has emerged as a

critical enabler of continued miniaturization, costefficiency, and improved yields.

"

As chip designs shrink and performance demands rise, EUV lithography is becoming essential in enabling the next generation of semiconductor technologies."

Sudip Saha

Major semiconductor manufacturers are heavily investing in EUV technology to meet the demands of next-generation chip architectures. With traditional photolithography reaching its physical and economic limits, EUV is being embraced for its ability to etch circuit patterns at wavelengths as short as 13.5 nanometers, enabling fabrication at 5nm, 3nm, and even more advanced nodes. Research and development efforts are

also being accelerated to improve EUV throughput, enhance source power, and reduce system downtime. These advances are instrumental in achieving higher production efficiencies and enabling commercial viability for mass-market chip production. The technology is particularly

vital for producing logic and memory chips used in smartphones, high-performance computing, autonomous vehicles, and Internet of Things (IoT) devices.

Propel Your Success: Secure Your Sample of Our Insightful Report Now! https://www.futuremarketinsights.com/reports/sample/rep-gb-5871

Key Takeaways from the EUV Lithography Market

The Extreme Ultraviolet Lithography market is expected to grow at a CAGR of 9.2% between 2025 and 2035, reaching USD 28.5 billion by the end of the forecast period. This growth is largely driven by increased capital expenditures from leading chip manufacturers and foundries such as TSMC, Samsung, and Intel, who are relying on EUV systems to push forward their sub-5nm manufacturing capabilities. The migration toward smaller nodes is imperative for delivering faster, more power-efficient chips for emerging digital applications. Furthermore, the integration of EUV technology into back-end-of-line (BEOL) processes is gaining traction, presenting additional revenue opportunities for equipment vendors.

The Asia-Pacific region is expected to dominate the market throughout the forecast period, supported by strong investments from leading chipmakers in Taiwan, South Korea, China, and Japan. North America and Europe also play a critical role, with significant contributions from key industry players and government-backed initiatives to promote advanced semiconductor manufacturing. In addition to commercial incentives, geopolitical factors are pushing countries to establish resilient domestic semiconductor ecosystems, leading to greater adoption of EUV technology across regions.

Emerging Trends in the Global Market

Among the most notable trends in the EUV market is the push toward High-NA (High Numerical Aperture) EUV systems, which offer enhanced resolution and depth of focus, enabling even finer chip patterning and greater design flexibility. Companies are also focused on developing advanced resist materials and pellicles that can withstand the intense EUV radiation while ensuring defect-free pattern transfer. The shift from single-patterning to multi-patterning EUV lithography is another growing trend, allowing chipmakers to extend EUV usage to more layers in the semiconductor manufacturing process.

The rising popularity of 3D chip stacking and heterogeneous integration is also driving the adoption of EUV, as it allows for precise layer alignment and scaling in complex chip structures. Additionally, there is a growing emphasis on automation, predictive maintenance, and Al-based diagnostics in EUV tools to reduce operational downtime and enhance system reliability. Sustainability is also gaining importance, prompting efforts to reduce energy consumption and improve source efficiency in EUV systems, making the technology more environmentally friendly and economically scalable.

Significant Developments in the Global Sector: Trends and Opportunities

The global race for semiconductor independence has prompted several governments to incentivize domestic production, opening up new opportunities for EUV system vendors. The U.S. CHIPS Act and the European Chips Act, for instance, have earmarked billions of dollars in funding to bolster semiconductor R&D and manufacturing infrastructure, including EUV lithography. In response, companies like Intel and GlobalFoundries have announced major fab expansion plans that include EUV tool installations.

Furthermore, strategic collaborations between equipment manufacturers and chipmakers are strengthening. Companies are working together to co-develop next-generation EUV solutions, including higher wattage light sources and advanced metrology systems to improve yield control. There is also a growing opportunity for specialized service providers offering maintenance, calibration, and EUV mask handling solutions, as the ecosystem around EUV matures and expands.

Recent Developments in the Market

ASML, the undisputed leader in EUV lithography systems, continues to lead the market with the rollout of its Twinscan NXE and next-generation High-NA platforms. In 2024, the company began shipping its High-NA EUV machines to early-access customers such as Intel, signaling a major shift toward sub-2nm node development. TSMC and Samsung have also expanded their EUV production lines and are exploring further integration of EUV into DRAM and NAND flash memory processes. Additionally, semiconductor material suppliers are ramping up production of EUV-specific photoresists and pellicles to meet rising demand.

Collaborations have intensified across the supply chain. For example, Tokyo Electron, Nikon, and other equipment vendors are developing complementary tools to support EUV mask inspection and etching. Governments are also offering grants and subsidies to foster domestic innovation in EUV tool manufacturing, particularly in regions like South Korea and Japan, where local firms aim to reduce dependency on foreign suppliers.

Comprehensive Industry Report: A Full Market Analysis https://www.futuremarketinsights.com/reports/extreme-ultraviolet-lithography-market

Competition Outlook

The EUV lithography market is characterized by limited competition due to the high entry barriers and the complexity of the technology. ASML Holding NV holds a near-monopoly in EUV lithography systems, supplying all major semiconductor foundries globally. However, competition is gradually emerging in related areas such as EUV mask blanks, pellicles, photoresists, and inspection tools, where companies like Carl Zeiss, KLA Corporation, and Tokyo Electron are key players.

Key Players

Leading players in the market include ASML Holding NV, Tokyo Electron Limited, Canon Inc., Nikon Corporation, Carl Zeiss AG, KLA Corporation, Lam Research Corporation, Applied Materials Inc., Toppan Photomasks, and JSR Corporation. These companies are heavily involved in either EUV system production or supplying critical subsystems, including optics, resists, and inspection equipment.

Key segmentations

Key segmentations in the EUV market include by equipment type (light sources, mirrors, mask systems, and others), by end-user (foundries, integrated device manufacturers, and outsourced semiconductor assembly and test providers), and by application (logic, memory, and others). Regionally, the market is divided into North America, Europe, Asia-Pacific, Latin America, and the Middle East & Africa, with Asia-Pacific maintaining the largest share due to the dominance of key semiconductor hubs.

Semiconductors Industry Analysis Reports

NFC Reader ICs Market Size and Share Forecast Outlook 2025 to 2035 https://www.futuremarketinsights.com/reports/nfc-reader-ics-market

Crystal Oscillator Market Size and Share Forecast Outlook 2025 to 2035 https://www.futuremarketinsights.com/reports/crystal-oscillator-market

Fiber Optic Gyroscope Market Size and Share Forecast Outlook 2025 to 2035 https://www.futuremarketinsights.com/reports/fiber-optics-gyroscope-market

Embedded Hypervisor Market Size, Share, and Demand Forecast 2025 to 2035 https://www.futuremarketinsights.com/reports/embedded-hypervisor-market

I2C Bus Market Size, Share, and Forecast Outlook from 2025 to 2035 https://www.futuremarketinsights.com/reports/i2c-bus-market

Ankush Nikam
Future Market Insights, Inc.
+91 90966 84197
email us here
Visit us on social media:
LinkedIn
Facebook
YouTube

This press release can be viewed online at: https://www.einpresswire.com/article/818552308

EIN Presswire's priority is source transparency. We do not allow opaque clients, and our editors try to be careful about weeding out false and misleading content. As a user, if you see something we have missed, please do bring it to our attention. Your help is welcome. EIN Presswire, Everyone's Internet News Presswire™, tries to define some of the boundaries that are reasonable in today's world. Please see our Editorial Guidelines for more information.

© 1995-2025 Newsmatics Inc. All Right Reserved.