

Mini machines, big impact: tackling distortion in PT-symmetric MEMS

GA, UNITED STATES, June 23, 2025 /EINPresswire.com/ -- Achieving unidirectional signal flow—where information travels in only one direction—is vital for next-generation communication systems. Micromechanical devices leveraging parity-time (PT) symmetry offer a promising route to this goal without



PT-symmetric silicon micromechanical resonators.

the need for bulky magnets. But, are there any distortions in these systems? In a groundbreaking study, researchers present the first experimental exploration of nonlinear distortion in siliconbased PT-symmetric resonators operating in a broken phase. They pinpoint critical performance thresholds, including a 1 dB gain compression point at 5 dBm and an intermodulation intercept point at 11.5 dBm, laying a foundation for more robust and compact nonreciprocal components.

Traditional nonreciprocal devices rely on magnetic materials to steer signals in one direction—an approach that poses significant challenges for integration into modern microelectronic systems. parity-time (PT)-symmetric systems, which feature gain and loss, have emerged as a novel way to break reciprocity using engineered asymmetries instead of magnets. These systems have demonstrated success across optics, acoustics, and mechanics. However, their nonlinear behavior, especially under higher signal strengths, introduces a new layer of complexity. Signal distortion from nonlinear gain threatens the very advantages these systems promise. Due to these problems, a detailed investigation into how PT-symmetric resonators behave under nonlinear stress is urgently needed.

A research team from Southeast University, China, has taken a critical step toward enabling chipscale nonreciprocal devices by experimentally probing how PT-symmetric silicon micromechanical resonators exhibit nonlinear distortion. Published on May 21, 2025, in Microsystems & Nanoengineering, their study fills a knowledge gap in <u>microelectromechanical</u> <u>systems</u> (MEMS) design by quantifying when and how signal fidelity degrades under increasing input power. Their findings not only validate theoretical models but also reveal unexpected contributors to distortion—bringing us closer to practical, miniaturized components for wireless and sensing applications. The researchers engineered a pair of silicon micromechanical resonators, one providing gain and the other loss, to operate in the "broken phase" of PT symmetry—a regime that enables nonreciprocal behavior. By applying external driving signals and adjusting vacuum and coupling conditions, they created a system where signals transmitted in one direction experience less attenuation than in the other. But the real test came when signal strength increased. The team discovered that at an input of 5 dBm, the gain began to compress—a key distortion threshold known as the 1 dB compression point. At 11.5 dBm, third-order intermodulation signals appeared, marking the IIP3. These distortions were influenced not just by nonlinear gain but also by inherent electrostatic forces and geometric softening in the silicon beams. This multi-source distortion challenges simple models and underscores the importance of comprehensive simulations. By aligning experiment with theory, the researchers provide the first quantitative framework for distortion in nonreciprocal micromechanical systems.

"Our study sheds light on what really happens inside PT-symmetric micromechanical systems when you push them hard," said Prof. Qing-An Huang, co-author of the study. "We now know that multiple nonlinear effects—not just gain—contribute to signal distortion. This insight allows us to fine-tune these devices for real-world applications and helps pave the way for reliable, miniaturized nonreciprocal components."

These findings have important implications for the design of next-generation MEMS-based isolators and circulators—key components in communication systems, radar, and signal processing. By identifying when and why distortion arises, engineers can now optimize PT-symmetric devices for higher signal fidelity and broader functionality. The results open a path toward compact, low-power nonreciprocal systems that can be seamlessly integrated into chips—transforming how we control signal flow at the microscale.

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